CLAIMS:

1.   A method for interfacing between first and second successive stages of an ASIC synthesis tool, said method comprising:

      (i)   receiving output data at an output of the first stage,

      (ii)   automatically generating first script files, and

      (iii)   feeding said output data and said first script files to the second stage for processing the output data in accordance with information contained in the first script files.

2.   The method according to Claim 1 further comprising:

      (iv)   obtaining second script files as an output of the first stage, and

      (v)   feeding said second script files to the second stage for processing the output data in accordance with information contained in the second script files.

4.   The method according to Claim 3 further comprising:

      (vi)   obtaining third script files from an external source, and

      (vii) feeding said third script files to the second stage for processing the output data in accordance with information contained in the third script files.

5.   The method according to Claim 1 including using a pre-prepared template for automatically generating the third script files.

6.   An apparatus for interfacing between first and second successive stages of an ASIC synthesis tool, said apparatus comprising:

      a processor,

      a plurality of selectors coupled to the processor and each corresponding to a tool for performing a stage in an ASIC design, and

      a memory coupled to the processor and storing therein respective command files relating to each of said selectors;

      said processor being responsive to selection of at least one of the selectors for accessing the memory and executing commands associated therewith.

7.   The apparatus according to Claim 7, wherein the commands executed by the processor are extracted from the respective command file relating to each of said selected selectors.

8.   The apparatus according to Claim 8, wherein the command file relating to each of said selected selectors is automatically generated.

9.   The apparatus according to Claim 8, wherein the command file relating to each of said selected selectors is automatically generated according to a pre-prepared template.

10.   The apparatus according to Claim 9, wherein the pre-prepared template is part of the selected selector.

11.   The apparatus according to Claim 10, wherein the pre-prepared template is a pre-prepared external file.

12.   The apparatus according to Claim 12, wherein the pre-prepared external file is an XML file.

13.   The apparatus according to Claim 7, wherein the commands executed by the processor are parts of each of said selected selectors.

14.   The apparatus according to Claim 7, wherein at least some of said selectors are operable via a graphical user interface.

15.   A program storage device readable by machine, tangibly embodying a program of instructions executable by the machine to perform method steps for interfacing between first and second successive stages of an ASIC synthesis tool, said method comprising:

    (i)   receiving output data at an output of the first stage,

    (ii)   automatically generating first script files, and

    (iii)   feeding said output data and said first script files to the second stage for processing the output data in accordance with information contained in the first script files.

16.   A computer program product comprising a computer useable medium having computer readable program code embodied therein for interfacing between

first and second successive stages of an ASIC synthesis tool, said computer program product comprising:

computer readable program code for causing the computer to receive output data at an output of the first stage,

computer readable program code for causing the computer to automatically generate first script files, and

computer readable program code for causing the computer to feed said output data and said first script files to the second stage for processing the output data in accordance with information contained in the first script files.